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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Application Number 09/997530

Filing Date 11/30/2001

First Named Inventor Master, et al.

Group/Art Unit 2661

Examiner Name Not Yet Assigned

Attorney Docket Number QST 2001.002

Total Number of Pages in This Submission

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Nancy R. Gamburd Gamburd and Associates, Ltd.
Signature	
Date	10/09/2002

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date: 10/09/02

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PRELIMINARY AMENDMENT

SERIAL NO. 09/997,530

Applicant: Master, et al.

Serial No.: 09/997,530

Group Art: 2661

Filing Date: November 30, 2001

Title: APPARATUS, SYSTEM AND METHOD FOR CONFIGURATION OF ADAPTIVE
INTEGRATED CIRCUITRY HAVING FIXED APPLICATION SPECIFIC
COMPUTATIONAL ELEMENTS

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To: Director of Patents
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PRELIMINARY AMENDMENT

Dear Sir or Madam:

In advance of the first office action, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please amend the specification in accordance to the below changes. A marked up version of the changes is attached hereto.

Page 11, starting on line 7, please replace the paragraph with the following:

91
A significant departure from the prior art, the ACE 100 does not utilize traditional (and typically separate) data, direct memory access (DMA), random access, configuration and instruction busses for signaling and other transmission between and among the reconfigurable matrices 150, the controller 120, and the memory 140, or for other input/output ("I/O") functionality. Rather, data, control and configuration information are transmitted between and among these matrix 150 elements, utilizing the matrix interconnection network 110, which may be configured and reconfigured, to provide any given connection between and among the reconfigurable matrices 150, including those matrices 150 configured as the controller 120 and the memory 140, as discussed in greater detail below.